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# Elliptical Curve Cryptography Using RSD 

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#### Abstract

In this paper a novel way of cryptography to generate secret key using RSD is presented. In this, a problem of security is formulated using elliptical curve method. First, a algorithms based on Karatsuba-Ofman and RSD methods helps us to generate the 8 bit key or introducing encryption. Finally, a simulation results has shown the accuracy of karatsuba algorithm and has the reduced the complexity from $n^{2}$ to $n^{1.58}$. Further the performanceis enhanced by using RSD algorithm for addition and subtraction.


## I.INTRODUCTION

Cryptography is the science of transforming messages. It makes them secure depending on the algorithm used for encryption and decryption. For example, for 80 bit information RSA algorithm gives 1024 bit key size. In this data, containing information is converted into encrypted data. The encrypted data consists of cipher text and data containing information.. Hence Cryptography provides a fair unit of confidentiality, authentication, integrity, access control and availability of data is maintained. Cryptography is portioned into two types Symmetric and asymmetric cryptography. Symmetric cryptography is also known as secret Key cryptography. Here same key is being used for encryption and decryption process, whereas same algorithm encrypts and decrypts the data, i.e. the keys are shared between the sender and receiver. The key must be kept secret. Asymmetric Cryptography is known as Public Key Cryptography. Here different keys are being used by sender and receiver for encryption and decryption process.
Elliptical curve cryptography is public key cryptography. It requires smaller key size as compared to non-ECC cryptography to provide equivalent security. For example, RSA requires 1024 bit key size whereas ECC needs only 160 bit key size for 80 bit information. For example, it provides equivalent security to RSA framework with smaller key sizes. Elliptical curve cryptography consists of two major operations Point Addition and Point Doubling, these two major operations are called as scalar point calculations. Points are doubled and being added through the series of operations like additions, subtractions and multiplications performed on the respective co-ordinates using geometrical properties.
A Redundant Signed Digit (RSD) is a numeral system. It uses more bits than required to represent a single binary digit. Hence most numbers will have several representations. For example radiax-2 RSD number is represented by $\{-1,0,1)$. RSD properties are different from the regular binary representation systems. Here RSD allows addition without using a typical carry. RSD makes bitwise logical operation slower, as it contains positive as well as negative values. Carry free addition is an attractive property of RSD as it do not generate carry while addition.
For reducing the multiplication complexity used by us during school days is given by $O\left(n^{2}\right)$.
The methodology proposed by Karatsuba and Ofman gives the complexity $O\left(n^{1.58}\right)$ for performing multiplication. Here the numbers to be multiplied are divided into smaller and equal segments. Recursive karatsuba produces three half-word instead of four half-word multiplications, with some addition and subtractions. In this for finding the points on the elliptic curve multiplication process is required which is given by karatsuba and ofman algorithm. The RSD numerical system used to represent the key in different polarities.

## II. LITERATURE SURVEY

An elliptical curve cryptography designed by Neal Koblitz and Victor Miller achieves better security and enhancement in the parameters like key findings and data encryption [1][2].The author has the elliptical curve defined by weierstrass equation helps to find the points of the curve by performing the operations like point addition and point doubling.

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This chapter gives information about the recent work going on the elliptical curve cryptography:
The author has proposed a P256 NIST (National Institute of Standards and Technology) curve processor with efficient modular addition, subtraction, multiplication and division operations. The author has also introduced an adder with carry free addition. The proposed adder works with 3 layers. The layer 1 gives result as $(\mathrm{n}+1)$. The layer 2 gives result as $(n+2)$ and the layer 3 finalizes the output as $(n+1)$ in order to eliminate carry. Author has proposed his work on Xilinx Virtex 5(XC5VLX110T) FPGA. The maximum frequency obtained is 160 MHz and to find the coordinates 2.26 ms time is required. [5]
The author has described a method to reduce the multiplication steps. Author performed multiplication of multi-digit numbers by reducing the computational complexity to a great extent. The author splits the two operands into low (L) and high $(\mathrm{H})$ segments. It reduces the complexity from $O\left(n^{2}\right)$ to $O\left(n^{1.58}\right)$. [3].
The author has explained not only theoretically but also simulation based result to estimate the modular division. Author has also given the architecture for the modular division. Here the author sets two flags $\rho$ and $\delta$ which first check for the odd or even for the dividend. The simulation gives results as -1 or +1 . [10]
The author had come up with hardware as well as as software for the modular multiplication. On the other hand the for hardware implementation he came up with a hardware algorithm for modular multiplication. [9]
The author has described VLSI design for the multipliers where we can use karatsuba algorithm and simulation based results are shown to estimate the multiplication result. [8]
The author has proposed a processor for the elliptical curve cryptography with a radiax-4 unified division unit. On the other hand the processor proposed is hardware and simulation based, he has proposed 160-bit and 256-bit DECP with $0.29 \mathrm{~mm}^{2}$ and $0.45 \mathrm{~mm}^{2}$ for the silicon area in 90 nm .[6].
The author has proposed a co-processor for increasing the computational speed of the elliptical curve scalar multiplications. The scalar multiplication here is introduced with point addition and point doubling operations. The author has showed the latency under $20 \mu s$ for a $2^{80}$ bit security.
The author has explained theoretically the concepts of cryptography and about the advantages and limitations of the cryptography techniques. [4]

## III. BACKGROUND

## A. Elliptical Curve Cryptography

Elliptical curve cryptography is public key cryptography. It requires smaller key size as compared to Non-ECC cryptography to provide equivalent security. For example, it provides equivalent security to RSA framework with smaller key sizes. Elliptical curves are defined over a field K by the reduced Weierstrass equation given in equation (1)[1]. The further points of the curve are obtained by the point addition and point doubling operation in which point addition is the basic operation.

$$
\begin{equation*}
E: y^{2}=x^{3}+a x+b \tag{1}
\end{equation*}
$$

The smoothness of the curve and the distinct roots are defined by $4 a^{3}+27 b^{2} \neq 0$. The curve obtained by th equation by performing the modulo a prime number. Such elliptical curves are called as prime field elliptical curves. The coordinates of the point addition and point doubling is calculated as follows, assuming $\mathrm{P}=\left(x_{1}, y_{1}\right), \mathrm{Q}=\left(x_{2}, y_{2}\right)$ and $\mathrm{R}=(P+Q)=\left(x_{3}, y_{3}\right)$ :
The conditions defined below to perform point addition and point doubling in equation (2) [3]:

$$
\begin{array}{ll}
\text { Point addition }: S=\frac{y 2-y 1}{x 2-x 1} \bmod p & \text { if } p \neq q \\
\text { Point doubling: } S=\frac{3 x^{2}+a}{2 y} \bmod p & \text { if } p=q \tag{2}
\end{array}
$$

Here according to above condition point addition and point doubling operation is performed, then for point addition the coordinates for $\mathrm{R}\left(x_{3}, y_{3}\right)$ is obtained as in equation (2) [3]:

$$
\begin{gather*}
x_{3}=\left(\left(\frac{y_{2}-y_{1}}{x_{2}-x_{1}}\right) \wedge 2\right)-x_{1}-x_{2} \\
y_{3}=\left(\frac{y_{2}-y_{1}}{x_{2}-x_{1}}\right)\left(x_{1}-x_{3}\right)-y_{1} \tag{3}
\end{gather*}
$$

Whereas the point doubling operation is obtained as follows in equation (2) [3]:

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$$
\begin{gather*}
x_{3}=\left(\frac{3 x_{1}^{2}+a}{2 y_{1}}\right)-2 x_{1} \\
x_{3}=\left(\frac{3 x_{1}^{2}+a}{2 y_{1}}\right)\left(x_{1}-x_{3}\right)-y_{1} \tag{4}
\end{gather*}
$$



Fig.1. Elliptical curve for the equation $E: y^{2}=x^{3}+a x+b$
The two operations point addition and point doubling is also called as point scalar multiplication. Here point scalar multiplication is the operation of multiplying a point P on the elliptic curve by an integer scalar k .

## B. Redundant Signed Digit

A Redundant Signed Digit (RSD) is a numeral system. It uses more bits than required to represent a single binary digit. Hence most numbers will have several representations. In effect it is called as redundant. RSD properties are different from the regular
binary representation systems. Here RSD allows addition without using a typical carry.
When compared to non-redundant representations, RSD makes bitwise logical operation slower, but arithmetic operations are faster when a greater bit width is used. Carry free addition is an attractive property of RSD.
A non-redundant radiax-r number has digits from the set $\{0,1, \ldots \ldots \ldots, r-1\}$, where $r$ is any number. Here the numbers can be represented in a unique way. Whereas, radiax-r RSD number is based on the digit set ' S 'in equation (5) [11].
$S=\{-\beta,-(\beta-1), \ldots \ldots \ldots,-1,0,1, \ldots \alpha\}$
Where $1 \leq \beta, \alpha \leq r-1$. A symmetric signed digit has $\alpha=\beta$.
A symmetric signed digit representation uses the digit set given below. Where r is the radiax and $\alpha$ is the largest digit in the set given in equation (6)[11].
$D_{\langle r, \alpha\rangle}=\{-\alpha, \ldots \ldots-1,0,1, \ldots . \alpha\}$
A number in this representation is written as in equation (7) [11]:
$X_{\langle r-\alpha\rangle}=X_{W-1} \cdot X_{W-2} \cdot X_{W-3} \cdot X_{W-4} \ldots \ldots . X_{0}=\sum X_{W-1-i} r^{i}$

## C. Karatsuba-Ofaman Multiplication

Multiplication is the basic step required in several advanced field like cryptography,
where several multiplication steps are required for every process. When multiplying two $\quad \mathrm{N}$ digit numbers we need $N * N=$ $N^{2}$ multiplication steps [3]. Here first n -digit number is split into two (the first part of the number is multiplied with some base and with the second part). With the help of intermediate products and base number final product is obtained. The complexity of addition operations is usually less than the complexity of multiplication operations. Henceforth the usage of Karatsuba algorithm is increased in several advanced fields.
Having two operands of length $n$ to be multiplied, the Karatsuba-Ofman methodology suggests to split the numbers into high $(\mathrm{H})$ and low (L) as in equation (8) [3]:

$$
\begin{equation*}
a_{H}=\left(a_{n-1}, \ldots \ldots, a_{\frac{n}{2}}\right), a_{L}=\left(a_{\left(\frac{n}{2}\right)-1}, \ldots \ldots, a_{0}\right) \tag{8}
\end{equation*}
$$

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$$
b_{H}=\left(b_{n-1}, \ldots \ldots, b_{\frac{n}{2}}\right), b_{L}=\left(b_{\left(\frac{n}{2}\right)-1}, \ldots \ldots, b_{0}\right)
$$

Consider $\beta$ as the base for the operands, where $\beta$ is 2 in case of integers and $\beta$ is x in case of polynomials. Then, the multiplication of both operands is performed a follows: $\quad a=a_{L}+a_{H} \beta^{\frac{n}{2}}$ and $b=b_{L}+b_{H} \beta^{\frac{n}{2}}$

Four half-sized duplications are required, where Karatsuba procedure reformulatein equation (9) [5] :

$$
\begin{align*}
C & =A B=\left(a_{L}+a_{H} \beta^{\frac{n}{2}}\right)\left(b_{L}+b_{H} \beta^{\frac{n}{2}}\right) \\
& =a_{L} b_{L}+\left(a_{L} b_{H}+a_{H} b_{L}\right) \beta^{\frac{n}{2}}+a_{H} b_{H}\left(\beta^{\wedge} n\right) . \tag{9}
\end{align*}
$$

Therefore only three half-sized multiplication are needed. Karatsuba algorithm is performed recursively, where the operands are segmented into smaller parts until a reasonable size is reached and the regular multiplication with smaller segments is performed.

## IV. SIMULATION OF ELLIPTICAL CURVE CRYPTOGRAPHY USING RSD

The elliptical curve cryptography by using RSD is used to generate the key which are nothing the points of the elliptic curve. This can be achieved by using the modular arithmetic operation. Here we are performing multiplication by using Karatsuba algorithm which reduces the multiplication steps and hence increases the speed of the multiplication. For key generation an equation of the curve is defined and the points are generated by the scalar operations and a LUT based structure for the curve is proposed which enhances the performance of the key generation.

## V. RESULTS



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Here $x a$ and $x b$ are the initial points of the elliptic curve. Here areprents the points on the elliptic curve, basically $a$ tells us the point number to be obtained. $x t$ and $y t$ are the next co-ordinates on the curve.

| Initial points | Obtained points |
| :---: | :---: |
| $x b=5$ | $x t=9$ |
| $y b=1$ | $y t=16$ |

## VI. CONCLUSION

The work generates the key, as the elliptic curve is defined as LUT based. The multiplication used for the point doubling operation has time complexity $n^{1.58}$ which reduces the multiplication steps. The key is generated and the LUT slices are used and maximum operating frequency is 169 MHz . RSD as a carry free representation is used which results in short datapaths and increase in frequency.

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