

(An ISO 3297: 2007 Certified Organization) Vol. 4, Issue 8, August 2016

Simulation of Z-source Inverter Fed Induction Motor Drive

Mayuri A. Patney, R.M. Autee

ME Student, Department of Electronics and Telecommunication, Deogiri Institute of Engineering and Management

Studies, Aurangabad (MS), India

Assistant Professor & H.O.D, Department of Electronics and Telecommunication, Deogiri Institute of Engineering and

Management Studies, Aurangabad (MS), India

ABSTRACT: The Z-source inverter is a recent topological invention in the field of power conversion. It has both buck and boost capabilities as they allow the operation of the inverter in the shoot though state. This feature of shoot through state is not permitted in traditional voltage and current source inverters as it would destroy the device. Z-source inverter employs a unique LC network in between the DC source and the inverter. By controlling this shoot through state, it is possible to produce any desired output ac voltage. This paper presents a Z-source network for induction motor drive. The proposed system is developed with the help of PIC microcontroller which generates the PWM pulses to drive the gate. Simple Boost Control PWM technique has been employed in this inverter to supply the motor with ac voltage. The proposed drive system is simulated using Matlab/Simulink.

KEYWORDS: Voltage Source Inverter (VSI), Current Source Inverter (CSI), Z Source Inverter (ZSI)

I. INTRODUCTION

An inverter is the circuit used to convert the dc input signal into alternating current output. Depending on the nature of the input source, inverters are classified as voltage source inverters and current source inverters. In case of voltage source inverters, input is provided by a dc voltage source, whereas in current source inverters, voltage source is first converted into current source and then it is used as an input to inverter i.e. in VSI a capacitor is used to obtain a constant voltage and current source inverters is that two switches of the same phase leg cannot be turned on/off simultaneously as it would destroy the device, hence dead time is required for safe operation. Also the obtainable output voltage range is limited, and the main bridge circuits of voltage and current source inverter are not interchangeable. Thus to overcome the drawback of traditional inverters, the concept of Z-source inverter is proposed. A Z-source based inverter system can

- 1) Can produce any desired output ac voltage ,even greater than the line voltage;
- 2) Provides ride through during voltage sags without any additional circuits;
- 3) Reduces inrush and harmonic current.

Z-source inverter employs a unique LC network in between the DC source and the inverter. With the LC network added, any two switches of the same phase leg can be switched on simultaneously resulting in a state called as shoot through state. This shoot through state is forbidden in traditional voltage and current source inverters. There are 7 different ways in which the shoot through can be achieved: shoot through via any one phase leg, shoot through via combination of two phase legs, shoot through via all three phase legs. This shoot through state provides the buck-boost feature to the Z-source inverter. The shoot through state boosts the capacitor voltage while producing zero voltage across the load. In this paper, modeling and simulation of the Z-source modulation and simulation is carried in Matlab/Simulink environment along with pulse width modulation control strategy.



(An ISO 3297: 2007 Certified Organization)

Vol. 4, Issue 8, August 2016

II. RELATED WORK

The Z-source inverter was proposed in 2002 by F.Z.Peng [1]. The Z-source inverter has an extra switching state, the shoot through state, which occurs when turning on both switches in the same phase leg. Based on the distribution of the shoot-through ,various pulse width modulation strategies[2-4] exist to control the Z-source inverter such as simple boost, maximum boost, maximum constant PWM, modified space vector PWM and sine carrier PWM. The advantage of using the Z-source inverters is that harmonic distortion is reduced as compared with the traditional inverters [5]. There exist diverse power converter topologies networks mainly derived from the Z-source network by modifying the original impedance network, or by rearranging the connections of inductors and capacitors [6].Z-source inverter finds vast applications in the area of solar, fuel applications etc. In [7], a low cost Z-source inverter for photovoltaic application is presented. The proposed inverter can control the inverter output power, track the PV panel's maximum power point, and manage the battery power, simultaneously. Fuzzy logic controlled Z-source inverter is proposed in [8].A standalone power generation system or UPS is implemented using Z-source inverter in [9]. A seven level diode clamped Z-source inverter for PV application provides higher output voltage through its Z-source network [10]. Z-source inverter based DG (distributed generation) system to improve power quality of distribution systems is proposed in [11]. Application of Z-source inverter in photovoltaic grid connected transformer-less inverter is presented in [12].

III.Z-SOURCE INVERTER

The Z-source inverter consists of unique impedance network placed in between the power source and the inverter circuit. This impedance network helps in providing buck-boost feature that cannot be observed with traditional inverters. The impedance network consists of two inductors and two capacitors connected to each other as shown. The inductors are connected in series arm and the capacitors are connected in diagonal arms. The dc voltage source can be a battery, fuel cell, photovoltaic array, diode rectifier.



Figure 1: Z-source inverter

To describe the operating principle and control of the Z-source inverter in Fig. 1, let us briefly examine the Z-source inverter structure. The Z-source network operates in two states: shoot through state and the non shoot through state. The non shoot through state is same as in the traditional inverter where the output voltage depends on inverter bridge and the modulation index. In the shoot through state however, the Z- source network boost the voltage across the capacitors thereby raising the voltage at the inverter bridge. In the shoot through state, the Z source network is shorted and in the non shoot state or active state, the Z source network sees the load.



(An ISO 3297: 2007 Certified Organization)

Vol. 4, Issue 8, August 2016

IV. MATHEMATICAL ANALYSIS



Figure 2:Equivalent circuit when ZSI is in shoot through state

The impact of the shoot through state on the inverter performance can be analysed using the equivalent circuit in figure 2. In shoot through state the inverter side of Z-Source network is shorted during time interval T₀. Therefore $L_1=L_2=L$ and $C_1=C_2=C$

Now consider that the inverter bridge is in nonshoot- through state for an interval of T_1 , during the switching cycle T, and from the equivalent circuit,



Figure3: Equivalent circuit when ZSI is in non shoot through state

 $V_L = V_{DC} - V_C \quad V_d = V_{DC} \quad V_i = V_C - V_L = 2 \ V_C - V_{DC}$

Averaging the voltage across a Z-source inductor over a switching period (0 to T) $VC = T1 / (T1 - T0) V_{dc}$

The peak DC-link voltage across the inverter bridge is

$$Vi=2V_{C}-V_{dc}=V_{dc}[(2T_{1} / T_{1}-T_{O}) - 1]$$

$$Vi=B. Vdc$$
Where,
B is a boost factor and is given by

 $B = [(2T1 / T1-T0) - 1] i.e. \ge 1$



(An ISO 3297: 2007 Certified Organization)

Vol. 4, Issue 8, August 2016

The peak ac output phase voltage, For Z- source

 $Vac = M.V_i / 2 = B.M \ Vdc \ / 2$ In the traditional sources

 $Vac = M. V_{dc} / 2$

Where,

M is modulation index.

Simple boost PWM strategy is employed to control shoot through of Z-source inverter. This strategy utilizes two straight lines/ envelope .When the triangular carrier waveform is greater than the upper envelope, V1, or lowers than the bottom envelope, V2, the circuit turns into shoot-through state. Otherwise it operates just as traditional carrier-based PWM. This method is less uncomplicated; however, the resulting voltage stress across the device is relatively high because some traditional zero states are not utilized either partially or fully.

V. SIMULATION RESULTS

Simulations have been performed. Figure 4 shows the circuit configuration of Z- Source fed Induction motor using simple boost PWM technique. The simulation parameters are as follows:

1. DC input voltage: 48V

2. Z-source network:

 $L_1 = L_2 = 1 \text{ mH}, C_1 = C_2 = 1000 \,\mu\text{F}$

3. Switching frequency: 10 KHz



Figure 4: Simulation model of Z- Source Inverter Fed IM

The following figure shows the simulation results of Z- source fed induction motor drive. Figure 5,6,7 shows the simple boost control technique implementation, output voltage of Z-source network and output current waveform.



(An ISO 3297: 2007 Certified Organization)

Vol. 4, Issue 8, August 2016



Figure 5: Simple boost control waveform



Figure 6: Z source boosted output



(An ISO 3297: 2007 Certified Organization)



Figure 7: Output current

VI. CONCLUSION AND FUTURE WORK

This paper has presented a new concept of inverters called Z-source inverters in which the impedance network is placed in between power source and inverter circuit. The paper describes the operating principle, circuit characteristics, and demonstrates its concept and superiority over traditional inverters. Z-Source inverter fed IM drive system is simulated using MATLAB/Simulink software. There are several PWM methods used to achieve voltage control within the inverter. Simulation of the Z-source inverter under simple boost control method using two straight lines is demonstrated. The Z-source inverter (ZSI) can be implemented by using maximum boost PWM technique also. A fuel cell system/solar panel can be used as the input source and effectiveness of the proposed control method can be verified.

REFERENCES

- 1. Fang Zheng Peng,, "Z-Source Inverter" IEEE transactions on industry applications, Vol. 39, No. 2, March/April 2003.
- Penchalababu.V, Chandrakala.B, Gopal Krishna, "A Survey on Modified PWM Techniques for Z-Source Inverter", International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249 – 8958, Volume-1, Issue-5, June 2012.
- S. Thangaprakash, A. Krishnan, "Comparative evaluation of modified pulse width modulation schemes of Z-source inverter for various applications and demands", International Journal of Engineering, Science and Technology Vol. 2, No. 1, 2010, pp. 103-115.
- 4. B.Y. Husodo, M. Anwari, and S.M. Ayob, "Analysis and Simulations of Z-Source Inverter Control Methods".
- K. Ravi Chandrudu and P. Sangameswara Raju, "Comparison of Z-Source Inverter Fed Induction Motor with Traditional Source Inverter Systems", Research Journal of Applied Sciences, Engineering and Technology 3(5): 386-392, 2011
- Siwakoti et al, "Impedance-Source Networks for Electric Power Conversion Part I: A Topological Review", IEEE transactions on power electronics, Vol.30, No.2, February 2015.
- S. Sudakar, S. Annadurai, "Z-Source Inverter Using Renewable Energy System," International Journal of Scientific Engineering and Research (IJSER), Volume 2 Issue 3, March 2014.
- Vijayabalan R, S. Ravivarman, "Z Source Inverter for Photovoltaic System with Fuzzy Logic Controller", International Journal of Power Electronics and Drive System (IJPEDS), Vol.2, No.4, December 2012, pp. 371~379.
- 9. A. Kulka and T. Undeland, "Voltage harmonic control of Z-source inverter for UPS applications", in Proc. 13th Power Electron. Motion Control Conf.
- 10. Ms.J.Kohila, Mr.R.Munia, Raj, Dr.S.Kannan, "Z-source multilevel inverter for photovoltaic application", International Journal of Innovative Research in Science, Engineering and Technology, Volume 3, Special Issue 3, March 2014.
- 11. Gajanayake et al, "Z-Source-Inverter-Based Flexible Distributed Generation System Solution for Grid Power Quality Improvement", IEEE transactions on energy conversion, Vol. 24, No. 3, September 2009.
- 12. Babak Farhangi and Shahrokh Farhangi, "Application of Z-source converter in photovoltaicgrid-connected transformer-less inverter", Electrical power quality and utilization, journal Vol.XII, No.2, 2006.