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Zero APT Technique for Evaluation of RESTORING Unbalanced Voltages

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ABSTRACT: Recently in Electrical power systems for dynamically coping with a range of voltage variations, Dynamic Voltage Restorers (DVRs) have been considered as most up-to-date series compensators. Their fast Response is the main advantage of DVRs, which can fulfil all most of requirements for sensitive loads. to enhance the abilities of DVRs Both the magnitude and phase displacement angle of the synthesized DVR voltage are precisely adjusted to achieve lower power utilization to maintain acceptable voltages and last longer during compensation.. The real and reactive powers are calculated in real time in the tracking loop to achieve better conditions. This technique results in less energy being taken out of the dc-link capacitor, resulting in smaller size requirements.

KEYWORDS: Zero APT (Zero Active Power Tracking), DVR, Real power, Reactive Power, Compensation, power Quality.

I. INTRODUCTION

VOLTAGE sags are one of the most important power quality issues [1]. The consequences of this type of occurrences are sensitive equipment dropout and possible full process or industrial-line disruption, with the obvious customer economic losses and complaints. Voltage sags usually last until network faults are cleared, and typically range from a few milliseconds to several seconds. The common procedure to mitigate voltage sags is to use dynamic voltage restorers (DVR), made of switching power converters (voltage source inverters) and energy storage (fig. 1), to reconstruct the network voltage. The DVR main function is to protect sensitive loads from voltage sags of the electrical network. Essentially, the DVR is a controlled voltage source inserted between the network and the critical load. The DVR injects a voltage on the network in order to correct any disturbance affecting the critical load voltage. Fig. 1 shows the schematic diagram of a typical DVR used for voltage correction. The sag detection, in conjunction with a suitable control scheme, is fundamental for the implementation of an effective DVR system. This is due to the requirement to switch the Several methods to detect voltage sags have been proposed, the most accepted ones being: 1) monitoring the peak values of the voltage supply; 2) monitoring of $\sqrt{Vd^2 + Vq^2}$ in a vector controller. 3) locking a narrow band pass filter or phase locked loop (PLL) to each phase; 4) applying the Fourier transform to each phase , and 5) applying the wavelet transform to each phase



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Fig 1 Schematic diagram of a typical DVR

In planning and operating today's stressed power systems, the ability to maintain voltage stability has become a growing concern. DVR will works on injection process, That means a compensation process, and is technically developed in many areas ex: front end/sources protection, midpoint protection, load end/ user end protection. In this paper we are proposing the new concept that is Zero Active Power Tracking (ZAPT) for evaluation of restoring unbalanced voltages. That it can be achieved only from generating the pulses to reduce the apparent power in the transmission system. The motto of Zero APT is for tracing the Active Power and compensates it through pulse generating by diode family converters, and it compensates the missing voltage Magnitude and phase angle. We know that the power system is in 3 stages, Generation Transmission and distribution, and this system is practically and physically designed System, its completely different from ideal system, hence the Practical system will have lesser efficiency compare to ideal. This is because of every material will have its own Resistance, capacitance and also inductance, due to this in power transmission the heavy losses will be occur. In power transmission the power will transfers from on stage to another, power quality is necessary, hence it is required protection from transients, flickers, and also from natural thunder effects. So protection required. So the proposal of ZAPT is suitable to handle this primary protection, Secondary protection, primary protection is for entire power system equipments purpose and secondary Protection is for power quality maintenance purpose. The combination of this two protections is improved in developed for the voltage magnitude restoring purpose. and it is named as DVR(dynamic voltage restorer) this will works on injection process. that means a compensation process and is technically developed in many areas ex:- front end/source protection, midpoint protection, load end/ user end protection to enhance the abilities of DVRs to maintain acceptable voltages and last longer during compensation. Both the magnitude and phase displacement angle of the synthesized DVR voltage are precisely adjusted to achieve lower power utilization. The real and reactive powers are calculated in real time in the tracking loop to achieve better conditions. This technique results in less energy being taken out of the dc-link capacitor, resulting in smaller size requirements this technique is developing day by day.... now they concentrating on power quality management by reducing number of elements, cost and also concern on Efficiency improvement. power it is combination of REAL power and , Reactive Power (VAR), complex power, Apparent power. ZAPT is main motto of tracking of Real power, if it is missing in terms of Magnitude and phase angles then it compensates through pulse generators



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II. CHARACTERISTICS OF VOLTAGE SAG

Voltage sag is characterized in terms of the following parameters,

- 1. Magnitude of sag
- 2. Duration of sag
- 3. Phase-angle jump.
- 4. Point on a wave

A. Magnitude of sag

Generally to characterize the sag magnitude is through the lowest per unit RMS remaining voltage during the event of sag. It means a deep sag is the sag with a low magnitude and shallow sag has a large magnitude.



Fig.2. Magnitude of voltage sag

B. Duration of sag

The duration of sag is mainly determined by the fault-clearing time. For fast clearing of the fault, duration of sag will be less and vice-versa. The duration of a sag in various standards is defined in the range from 0.5 cycle to one minute. It should be noted that the voltage reduction events shorter than 0.5 cycles influence the sensitivity of some equipment measurement and others are deliberate, using specifications that anticipate your paper as one part of the entire proceedings,

C. Phase-angle jump

The Phase-angle jump manifests itself as a shift in zero crossing of the instantaneous voltage .In three phase faults Phase-angle jumps are due to a difference in the X/R ratio between the source and the feeder. Most of the equipment are not affected by Phase-angle jumps but power electronic converters using phase angle information for their switching may be affected.

D. Point on wave

To obtain exact value for the sag duration information of "start" and "ending" of the sag is necessary. For this one needs to find the "point-on-wave of sag initiation" and the "point-on-wave of voltage recovery". Both require more advanced analysis techniques, which are still under development.

Solutions to Power Quality Problems

The solution to the power quality can be done from customer side or from utility side. First approach is called load conditioning, which ensures that the equipment is less sensitive to power disturbances, allowing the operation even under significant voltage distortion. The other solution is to install line conditioning systems that suppress or counteracts the power system disturbances. A flexible and versatile solution to voltage quality problems is offered by active power filters. Currently they are based on PWM converters and connect to low and medium.



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III. DETECTION TECHNIQUE OF VOLTAGE SAG

A. Voltage sag detection method

The proposed method for the detection of voltage sags is based on the alfa/beta vector magnitude $V\alpha\beta$, which has a constant nominal value when no voltage sags occur. Therefore, it is only necessary to apply the Concordia transformation, and to calculate the alfa/beta vector magnitude to be able, by comparison with the nominal value, to detect symmetrical or asymmetrical voltage sags.

$$\binom{V\alpha}{V\beta} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -0.5 & -0.5 \\ 0 & 0.866 & -0.866 \end{bmatrix} \frac{Va}{Vc} eq(1)$$

In the Concordia transformation, the subscripts a, b and c denote the source phase voltages.

$$\left|\overline{V_{\alpha\beta}}\right| = \sqrt{V_{\alpha}^{2} + V_{\beta}^{2}} \qquad \text{eq(2)}$$

This method is complemented with a PLL to obtain constant amplitude sinusoidal Alfa/beta reference voltages $Vref_{\beta}$

B. Voltage reference generation

The dc/ac converter should control the transformer voltages in order to compensate for any disturbance affecting the load voltage. The voltage reference signals for the sliding mode controller are obtained in the alfa/beta reference frame, as seen in Fig.



Fig.3.Principle of the voltage reference frame



Fig.4.Reference vector generation topology



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The choice of the voltage injection strategy for sags and swells mitigation is directly related to the energy storage system dimensioning strategy and directly influences the amount of stored energy, the choice of its tapping from the mains, and the allowable minimum DC voltage on the DC link . The injected voltage can be in phase or may have a phase shift with respect to the mains voltage during the voltage sag. As the injected voltage is phase shifted with respect to the mains voltage from zero to 90° degrees, the required stored energy varies from the maximum to its minimum value. If the injected voltage is kept with a 90° shift from the load current, it is possible to replenish load voltages without the need of active power injection. In this case the minimum allowable DC link voltage amplitude and the inverter apparent power increase . In this DVR prototype, the energy is stored in a capacitor bank which is charged by means of a bridge rectifier connected to the mains . The dimensioning criteria is based on injection in phase with respect to the mains voltage.

The Proposed Compensation Technique

The goal of zero active power tracking for enhancing DVR-based compensation is to make the most of the utilizable energy (1WDC) that is stored in the DC-link capacitor. This limited energy can be calculated as in (7); due to technical limitations, not all the stored energy can be utilized. The minimum DC-side voltage (VDCmin) must be at a level that can still provide the converter with proper operation. Anything lower than this level will result in the converter not producing the demanded AC voltage. In practice, this value can be dened as in (8). In addition, by the principle of DC-to-AC power conversion, the maximum DC voltage (VDCmax) at the DC-link should increase proportionally to the AC-side voltage (VDVR) required for every instant active compensating actions. According to these two equations, the compensation period (TC) can be given as in (9), which shows that more utilizable energy yields longer active compensation for the DVR to address sags. Obviously, less real power utilized by the DC-link capacitor means that the DVR can also last for a considerable period and overcome more of the voltage sag.

$$\Delta W_{dc} = \frac{1}{2} C \left(V_{DCmax}^2 - V_{DCmin}^2 \right) \qquad \text{eq(3)}$$

$$V_{DCmin} \ge \sqrt{2V_{DVR}}$$
 eq(4)

Therefore, in order to gain more from the DVR, the idea proposed here is to achieve a condition where the compensation process requires the least real power from the DC-link capacitor.

A. Zero Active Power Tracking

As mentioned earlier, varying both the magnitude and phase of the DVR voltage plays a major role in reaching better compensation for the sag voltage. This voltage is involved in the calculation of energy stored in the DC-link capacitor that would be utilized each moment, as indicated in (1)-(6). Considering these equations, to achieve the best performance, the displacement of the corresponding DVR's voltage and load current complex phasors must be maintained at an angle of approximately 90 degrees, as illustrated in Fig. 4, ideally following calculations using (1) and (5) for zeroing the utilized energy on its own.



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Fig.6. Zero active power tracking compensation.

The illustration in Fig. 4 also shows that the DVR voltage phasor is a stretchable one, as required, representing the compensation voltage added to the sag voltage. Its magnitude is adjusted related to the calculation of (10), and its displacement angle α is set according to (11). An angular displacement indicates the load characteristic, which can be determined directly using (12), relating to the load real power. Similar to the angle displacement, it is also tied to the resulting voltage. Therefore, adjusting the DVR voltage will result in both and being changed. By this technique, the resulting load voltage phasor may or may not be returned to the pre-sag level; some degrees displacement must be observed. The tracking process starts with the control of the converter output to provide the corresponding voltage, which must be varied to meet the targets. The magnitude and phase are increased progressively until reaching a value that results in a relatively acceptable power. Two possible targets are set as the preferred tracking boundaries. In case 1, the ideal zero active power ($P_{DVR} = 0$) is achieved, while the load is still supported by some of the system reactive power. In this case, the load current lags behind the grid voltage (the sag voltage, V_{Sag}). In case 2, the minimum active power is achieved (as it cannot achieve zero active power), whenever all of the load reactive power is completely supported by the DVR and no longer supported by the system. At that point the load current appears to advance in leading the voltage sag, which indicates that the DVR not only supplies to the load side but also begins supplying reactive power back to the grid side. The increase in the phase angle α should be stopped immediately. If not, high power loss will degrade the DVR performance. Reconsidering the phasors of voltage and current shown in Fig. 4, it can be concluded that the normal and light voltage sags could possibly result in case (1) - the compensation eventually results in the zero reactive power condition. If the voltage sag is deep in a severe example, the compensation could result in the minimum power case 2. The effectiveness of this compensation process is directly reliant on a proper control system that assists the entire tracking process in order to achieve the mathematical targets detailed in the following.

$$|V_{DVR}| = \sqrt{V_L^2 - 2V_L V_{sag} \cos\left(-\theta + \gamma + \phi\right) + V_{sag}^2} \qquad \text{eq(6)}$$

$$\alpha = tan^{-1} \left(\frac{V_l \sin(-\theta + \gamma) - V_{sag} \sin(\theta)}{V_l \cos(-\theta + \gamma) - V_{sag} \cos(\theta)} \right) \qquad \text{eq}(7)$$

$$\gamma = \emptyset - \cos^{-1} \left(\frac{P_{load}}{V_{sag} I_L} \right) \qquad \text{eq(8)}$$

 $P_{DVR} = V_{DVR} I_l \cos(\alpha - \gamma) = 0 \qquad \text{eq}(9)$

 $Q_{DVR} = V_{DVR} I_l \sin(\alpha - \gamma) \qquad \text{eq(10)}$

$$Q_{S} = V_{SAG} I_{l} \sin(\gamma - \emptyset) \qquad \text{eq}(11)$$



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IV. CONTROL OF SELF SUPPORTED DVR

The control algorithm for self supported DVR is based on the principle of unit vectors and instantaneous symmetrical component theory for fundamental voltage extraction. The block diagram of the control system is shown in fig. Voltage sag, swell and harmonics can be compensated using this technique. The voltages at PCC (Vabc), load currents (IL), load voltages (VL) and DC voltage (VDC) are sensed for deriving the Insulated Gate Bipolar Transistor (IGBT) gate signals. The reference load voltage (VL *) is derived as image



Fig 7.Control block diagram of self supported DVR

Where VLa= Vta1- Vad+ Vaq is the reference load voltage of phase A in abc frame. Vta1 is the fundamental positive sequence voltage. Vad corresponds to required active voltage component of phase A for self supporting DC bus. Vaq is the required reactive voltage component for voltage regulation at load terminal. Reference load voltage of the remaining phases can be determined similarly. The active and reactive components of the voltage to be injected by the DVR are estimated using two PI controllers. In phase unit vectors are derived using samples of load current as, Two Proportional Integral (PI) controllers are used for regulating the DC bus voltage and the load voltage to their reference values. The reference load voltage and sensed load voltages are fed to the PWM controller for generating the gate pulses for the converter switches.

Voltage Injection Methods of DVR

Voltage injection or compensation methods by means of a DVR depend upon the limiting factors such as DVR power ratings, various conditions of load and different types of voltage sags. Some loads are sensitive towards phase angle jump and some are sensitive towards change in magnitude and others are tolerant to these. Voltage injection or compensation methods by means of a DVR depend upon the limiting factors such as DVR power ratings, various conditions of load, and different types of voltage sags. Some loads are sensitive towards phase angel jump and some are sensitive towards change in magnitude and others are tolerant to these. Therefore the control strategies depend upon the type of load characteristics.

Equations Related to DVR

The system impedance ZTH depends on the fault level of the load bus. When the system Voltage (VTH) drops, the DVR injects a series voltage VDVR through the injection transformer so that the desired load voltage magnitude VL can be maintained. The series injected voltage of the DVR can be written as

	VDVR= VL + ZTHIL - VTH	eq(11)
Where		_
3.71		

VL:	The desired load voltage magnitude
ZTH:	The load impedance.
IL:	`The load current
VTH:	The system voltage during fault condition



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The load current IL is given by,

$I_{\mathrm{L}} = \frac{P_{\mathrm{L}} + jQ_{\mathrm{L}}}{V}$	eq(12)
When VL is considered as a reference equation can be rewritten as, $V_{DVR} \angle 0 = V_L \angle 0 + Z_{TH} \angle (\beta - \theta) - V_{TH} \angle \delta$	eq(13)
β, δ are angles of Z _{TH} , V _{TH} , V _{DVR} respectively and θ is Load power angle $\theta = \tan -1 \left(\frac{\theta_L}{P_L}\right)$	eq(14)
The complex power injection of the DVR can be written as,	

$$S_{DVR} = V_{DVR} I_L^* \qquad eq(15)$$

It requires the injection of only reactive power and the DVR itself is capable of generating the reactive power.

V. EXPERIMENTAL RESULTS

The corresponding system for verification of the proposed Compensation technique was modeled in MatLab/Simulink/PowerSys for comparison with the traditional technique, as shown in the schematically simplified diagram in Fig. 9. The main components in this system are full-bridge power converters with LC filters and DC-link capacitor (C) included, three insertion transformers used as coupling devices series connected between a three-phase power supply and a lagging power factor load, and the control that monitors the system voltage (VPCC), load voltage (VL) and current (IL), as mentioned previously.



Fig.8. .Circuit diagram model for simulation using MatLab/Simulink.

TABLE 1. Parameter ratings for simulations and experiments.

Quantity	Value
Vs	380V
LOAD	2.33KVA,0.45 lagging
DC link Capacitor	3.3mF
LC Filter	1mH,20µF
Z _f	0.43Q 1mH



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Output signals from the control (G1-G12) were used for further gating of all the IGBTs in order to have the synthesized compensation voltage at the insertion transformer terminals, according to the technique employed. In comparison to this simulation, all the model's parameters were established relative to the overall test rig availability for the experimental tests and were scaled to available laboratory ratings. As shown in Table 1, a three-phase programmable supply of up to 380 V AC was used as the system source voltage (VS). The simulations were then conducted comparatively, as described in subsections A and B.

A. The Proposed Compensation

In order to extend the compensation as described in the previous sections, the proposed technique of zero-real power tracking was implemented with the same system mentioned in subsection A instead of the in-phase technique. The same simulation procedures were conducted, and the results were captured, as shown in Figs. 9-11. It is clear that different results were seen. The middle trace in Fig. 10 shows that by this technique, the DVR can certainly cover the entire voltage sag period, from the beginning at the time t D 0.4 s to the end at time tD0.9 s. The associated Quadrature voltages of both the DVR output and load point illustrated in Fig. 9 were increased and given the relevant voltages level led back to the desired value, as expected. It can be seen that the resulting three phase load voltage (the bottom waveform of Fig. 8) remained constant during the demand and after the sag period, which was a promising result. According to this technique, much lower real power was drawn from the DC-link capacitor, which resulted in relatively less energy being output for the instant compensation. Therefore, the decrease in DC-side voltage at the DC-link was not considerable, as seen in Fig. 11, compared with in-phase compensation illustrated in Fig. 9. Only a 50 V drop is seen, compared to 250Vfor the in-phase technique.



Fig 9. The overall three-phase voltage signals during zero-real power tracking compensation



Fig 10.Real power at source (PS), the DVR (PDVR) and load (PL) zero-real power tracking compensation



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Fig 11.The DVR DC-side voltage (VDC) during zero-real power tracking compensation simply that a considerable amount of energy remained for Possible further compensation.

VI. CONCLUSION

It is clear from both the simulation and experimental results illustrated in this paper that the proposed Zero-real power tracking technique applied to DVR-based compensation can result in superior performance compared to the traditional in-phase technique. The experimental test results match those proposed using simulation, although some discrepancies due to the imperfect nature of the test circuit components were seen. With the traditional in-phase technique, the compensation was performed and depended on the real power injected to the system. Then, more of the energy stored in the DC-link capacitor was utilized quickly, reaching its limitation within a shorter period. The compensation was eventually forced to stop before the entire voltage sag period was finished. When the compensation was conducted using the proposed technique, less energy was used for the converter basic switching process. The clear advantage in terms of the voltage level at the DC-link capacitor indicates that with the proposed technique, more energy remains in the DVR (67% to 14% in the traditional in-phase technique), which guarantees the correct compensating voltage will be provided for longer periods of compensation. With this technique, none (or less) of the real power will be transferred to the system, which provides more for the DVR to cover a wider range of voltage sags, adding more flexible adaptive control to the solution of sag voltage disturbances.

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