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# Comparative Studies on Intel 8085 Microprocessor and Intel 8051 Microcontroller With Some Suitable Assembly Language Programs

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**ABSTRACT:** Microprocessors are becoming an essential part of human life. Knowingly are unknowingly everyone is using the services of microprocessors because in almost all electronic home appliances like Air conditioners, Refrigerators, Washing machines, Mobile phones, Internet, microwave ovens etc., microprocessors are involved. The most useful and familiar microprocessor is 8085 microprocessor. The most useful processor for control applications is 8051 microcontroller. Although both are 8 bit processor, they possess many similarities and lot of differences in architectural, instructional and addressing modes point of view and also in writing and executing the assembly language programs. Therefore a systematic analysis was carried out to explain the similarities and the differences of 8085 & 8051 in all the above aspects.

Compare with the 8085 microprocessor, 8051 microcontroller has more number of registers and more special functional registers (SFRs). The superior quality of microcontroller is the accessibility of individual bits in many of the special function registers and all four ports of 8051 microcontroller. The 128 bytes of on chip RAM and 128 bytes of external RAM is playing an important role in the 8051 microcontroller. For loading and transferring the data into the registers only MOV instruction is used in 8051 microcontroller, where as MVI, LXI instructions are used for loading the data and MOV instruction transfers the data in 8085 microprocessors. The MUL, DIV and SWAP instructions are the advanced instructions available in 8051. It is mandatory to push a pair of registers in 8085 microprocessors where as the individual registers can be pushed in to the stack in 8051 microcontroller by using direct addressing mode. Because of DAD instruction, the double addition is more comfort with 8085 microprocessor than 8051 microcontroller, but, the availability of MUL & DIV instructions, executing multiplication and division programs with 8051are easier than in 8085 microprocessor.

**KEYWORDS**: Microprocessors, Microcontrollers, Registers, Assembly Language Program, Double Addition, Architecture and Instruction.

### **1. INTRODUCTION**

**INTRODUCTION TO MICROPROCESSORS:** Microprocessor is an electronic device used to process the data from memory to registers or register to register or from memory to outside world and vice versa within microseconds of time. It is a multipurpose programmable logic device that reads binary instructions from a storage device called memory, accepts binary data as input and possesses data according to those instructions and provides results as output. The microprocessor operates in binary digits 0 and 1 also known as bits. These digits are represented in terms of electrical voltages (GND and 5Volts) in the machine as shown in figure 1.



(An ISO 3297: 2007 Certified Organization)

### Vol. 4, Issue 4, April 2016

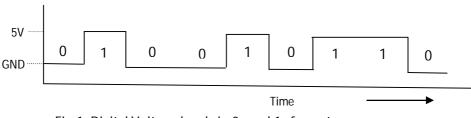


Fig.1. Digital Voltage levels in 0s and 1s format

Each microprocessor recognizes and processes a group of 8 bits (byte) called the word. A processor with an 8-bit word is known as an 8-bit processor. The microprocessor can be divided into 3 segments as shown in Fig.2.

**ARITHMETIC AND LOGICAL UNIT (ALU):** It is the area of the microprocessor where various computing functions are performed on data. The ALU performs arithmetic and logical operations and store the results either in registers or in memory.

**REGISTER ARRAY:** It consists of various registers. These registers primarily used to store data temporarily during the execution of a program. Some of the registers are accessible to the user through instructions.

**CONTROL UNIT:** It provides the necessary timing and control signals to all the operations in the microprocessors. It controls the flow of data between the microprocessor and memory and peripherals.

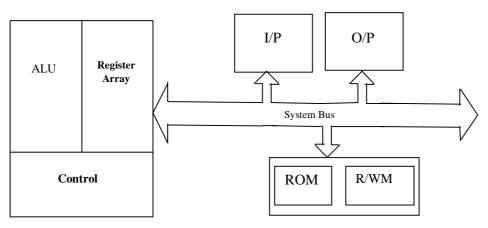


Fig.2. Block diagram of a general purpose microprocessor

**INTRODUCTION TO 8085 MICROPROCESSORS:** The Intel 4004 was the first 4 bit PMOS programmable device, which was introduced in the year 1971 and that was primarily used in calculators. It was quickly replaced by the 8 bit microprocessor (the Intel 8008), which was in turn superseded by Intel 8080. In mid 1970s, the Intel 8080 was widely used in control applications and small computers also were designed using the 8080 as the CPU. These computers become known as microcomputers. Within a few years after the emergence of 8080, the Motorola 6800, the Zilog Z80 and the Intel 8085 microprocessors were developed as improvements over the 8080<sup>(1)</sup>. As the microprocessor began to acquire more and more computing functions, they were viewed more as CPUs rather than as programmable logic devices. The 8 bit microprocessors are not simply being replaced by more powerful microprocessors; most microcomputers are now built with 32 and 64 bit microprocessors. However, each microprocessor has begun to carve a role for its own applications. The 8 bit microprocessors are being used as programmable logic devices in control applications.

**INTRODUCTION TO 8051 MICROCONTROLLERS:** In 1976, Intel introduced the 8048 series of single chip microcontrollers. It is also known as MCS-48<sup>(2)</sup>. The microcontrollers of this series are: 8048, 8041, 8042, 8049 and



(An ISO 3297: 2007 Certified Organization)

#### Vol. 4, Issue 4, April 2016

8035. These microcontrollers were popular and widely used for automatic control applications. These microcontrollers do not have multiplication and division instructions. In 1981, Intel Corporation introduced a more powerful 8-bit microcontroller called the 8051, generally referred as MCS-51. These are the second generation microcontrollers. It has 128 bytes of RAM, 4K bytes of on chip ROM, two timers, one serial port and four ports (each 8 bits wide) all on a single chip. The 8051 is an 8-bit processor, meaning that the CPU can work on only 8 bits of data at a time. Data larger than 8 bits has to be broken in to 8 bit pieces to be processed by the CPU<sup>(3)</sup>. The total 256 data's are appeared in Hexadecimal digits in the range from 00–FFH <sup>(4)</sup>. The 8051 has became widely popular after Intel allowed other manufacturers to make and market any flavour of the 8051 they please with the condition that they remain code compatible with the 8051. Although the 8051 is the most popular member of the 8051 family, it is not appear in the part number, because it is available in different memory types such as UV-EPROM version of the 8051 is the 8751, the flash ROM version is marketed by many companies including Atmel Corp. The Atmel Flash is called AT89C51. The NV-RAM version of the 8051 made by Dallas Semiconductor is called DS5000. The AT89C51-12PC, where C before 51 is for CMOS, which has a low power consumption, 12 indicates 12 MHz, P is for plastic DIP package and C for commercial (Vs. M for Military) is ideal for many student projects and research work.

### **II. EXPERIMENTAL TECHNIQUES**

The assembly language programs of 8085 were executed on MPS 85-3 trainer kit<sup>(5)</sup> which is based on 8085 CPU and 32K bytes of CMOS static RAM are provided with battery backup option. Total on board memory can be up to 64K Bytes. The 8085 is operated at 3.072 MHz and the microcontroller used in the present work is the SDA 51-MEL is a system design aid for operation of microcontroller devices<sup>(6)</sup>. It uses 8031/51 as the controller. It is a powerful microcontroller trainer for the INTEL 8051 microcontroller and it is operating at 11.0592 MHz.

### **III. RESULTS AND DISCUSSIONS**

**THE ARCHITECTURAL SIMILARITIES AND DIFFERENCES OF 8085 AND 8051:** Microprocessor is becoming an essential part of human life like electric power. In all most all electrical appliances like Refrigerator, Air conditioner, Mobile Phones, Televisions, Washing machines, Microwave ovens etc are controlled by microprocessors. The most useful and popular microprocessor is 8085 and the popular microcontroller is 8051.

Table 1. Similarities and the differences of 8085 microprocessors and 8051 microcontroller

S.	In 8085 microprocessors	In 8051 microcontroller			
No.					
1	RAM, ROM, timers, serial port and four ports (each 8	CPU, 128 bytes of RAM, 4K bytes of on chip ROM, two			
	bits wide) all are connected externally to the CPU.	timers, one serial port and four ports (each 8 bits wide) all			
	Therefore it is a general purpose microprocessor	on a single chip. Therefore it a microcontroller.			
2					
3	The total addressing capability of a CPU is $2^{16}$ bytes =	The total addressing capability of a CPU is $2^{16}$ bytes = 64			
	64 Kilo bytes, 1 Kilo = 1024 in computers. The address	Kilo bytes, 1 Kilo = 1024 in computers. The address are			
	locations are from 0000 to FFFFH	from 0000 to FFFFH			
4	8-bit Accumulator, which is associated with ALU. Bit	8-bit Accumulator is closely associated with ALU. Its			
	accessible registers are not available in 8085.	location is E0H of RAM. It is also a bit accessible register			
5	Six 8-bit general purpose registers. They are B, C, D,	It has four banks of registers and each bank has eight 8-bit			
	E, H and L. To hold a 16-bit data a combination of two	registers R0, R1, R2, R3, R4, R5, R6 and R7. Data bits D4			
	8-bit registers can be employed. The valid register pairs	and D3 of PSW are used to select the desired register bank.			
	are BC, DE and HL. The system monitor utilizes RAM	First 32 bytes of 128 bytes of RAM are set aside for the			
	locations 8F90H to 8FFFH for Stack and variables.	register banks and the stack.			
6	Size of Program Counter is 16 bits. It keeps track of	Size of Program Counter is 16 bits. It keeps track of			
	memory addresses of the instructions in a program.	memory addresses of the instructions in a program.			
7	Size of Stack Pointer is 16 bits. Generally highest	Size of Stack Pointer is 8 bits. By default, 07 <sup>th</sup> location of			
	addresses of the memory can be used as stack.	RAM is acts as SP. 30H-7FH of RAM can be used as stack.			



(An ISO 3297: 2007 Certified Organization)

### Vol. 4, Issue 4, April 2016

8	The HL pair is used to act as memory pointer and it	The 16 bit data pointer (DPTR) to hold 16-bit address. It			
0	holds the 16 bit address of a memory location	consists of DPH and DPL to hold 8-bit data.			
9	The five status flags are available in 8 bit Flag register	8-bit Program status word (PSW) register contains program			
	as shown below. They are Carry, Parity, Axiallary	status information as shown below:			
	Carry, Zero and Sign Flags.				
		CY AC FO RS1 RS0 OV P			
	S Z X AC X P X CY	Only 4 flag bits Carry, Axiallary Carry, Overflow and			
	Program status word is the combination of Flag register	Parity flags and RS1&RS0 bits are used to select the bank			
	and Accumulator. It is a 16 bit register and it is used to	of registers. PSW is a bit accessible register and its bits are			
	store the flag conditions in the Stack memory.	accessed as PSW.0 – PSW.7			
10	Instruction register holds the opcode of the instruction	There are number of special function registers (SFRs). The			
	which is decoded and executed. Temporary register	locations of SFRs are from 80H – FFH of RAM. A, B, PSW			
	associated with ALU holds data during an arithmetic /	and DPTR are part of SFRs. The SFRs can be accessed by			
	logical operation. It is used by the processor and not	their names or by their addresses. Some of the SFRs have			
	accessible to programmer.	the capability of single bit accessibility.			
11	<b>Interrupts:</b> The five interrupts with their service	The six interrupts and their interrupt service routines on			
	routines on their priority are TRAP $\rightarrow$ 0024H; RST	their priority are as follows: Reset $\rightarrow$ 0000H,			
	$7.5 \rightarrow 003$ CH; RST $6.5 \rightarrow 003$ 4H; RST $5.5 \rightarrow 002$ C,	External hardware interrupt 0 (INT0) $\rightarrow$ 0003H			
	INTR $\rightarrow$ It is sampled by the microprocessor in the last	Timer 0 interrupt (TF0) $\rightarrow$ 000BH, External hardware			
	state of the last machine cycle of each instruction. The	interrupt 1 (INT1) $\rightarrow$ 0013H, Timer 1 interrupt (TF1) $\rightarrow$			
10	RST n instructions are used for software interrupt.	001BH, Serial COM interrupt (RI and TI) $\rightarrow$ 0023H			
12	The device has 40 pins requires +5V single power	Available in 40 pin, in which 32 pins are reserved for 4			
	supply, operate with a 3 MHz single phase clock. All	multiplexed address and data ports (Port 0. Port 1, Port 2			
	signals classified in 6 groups(i) Address bus (ii)	and Control port) and 2 pins for power supply, 2 pins for			
	Multiplexed data bus (iii) Control and status signals	XTAL oscillator, 1 ALE pin to select address or data $\overline{\mathbf{DCEN}}$			
	(iv) Power supply&frequency signals (v) External	through ports, $\overline{EA}$ for external access, $\overline{PSEN}$ for external			
	initiated signals and (vi) Serial input and output ports.	program memory and RST for resetting the device			

There are several books available in the market to give deep subject about 8085 microprocessors and also for 8051 microcontrollers, but there are no sufficient material is available to compare the functions and their performances of 8085 microprocessors and 8051 microcontroller at a single flat form. Therefore, a systematic analysis was carried out by writing and executing some assembly language programs of both the processors to verify the functions and their performances of both the processors. The architectural similarities and differences of 8085 microprocessors and 8051 microcontroller were shown in Table 1.

A powerful feature of 8051 I/O ports is their capability to access individual bits of the port without altering the rest of the bits in that port. There are 16 bytes in the RAM from 20H to 2FH are reserved for single bit addressability. The individual bits of these bytes can be accessed as 00H – 7FH. On far with regular registers, there are several special function registers available in 8051. They are DPTR, IP, IE, TMOD, TCON, Two 16 bit timers, SCON, SBUF and PCON etc.; In 8051 the data pointer register can be used as a memory pointer to access data from external ROM. The instruction used is MOVX A,@DPTR, where X stands for external ROM. The DPTR register can accessed as DPH and DPL. There are two 16 bit timers Timer 0 and Timer 1 are used to generate time delays or to count the events happening in the processor (event counter). The timer 1 register can be accessed as TH1 and TL1, timer 0 register can be accessed as TH0 and TL0. An interrupt is an external or an internal event that interrupts the CPU to inform that a device needs its service. The program which is associated with the interrupt is called interrupt service routine. Various interrupts used by the 8085 microprocessor and 8051 microcontroller were also given in Table 1. Because of space limitations, complete analysis of instructions, timing analysis and the similarities and differences in interfacing concepts with various devices with 8085 microprocessor and 8051 microcontroller is not undertaken in the present work.

**THE INSTRUCTIONAL SIMILARITIES AND DIFFERENCES OF 8085 AND 8051:** To compare the similarities and the differences of assembly language instructions of both 8085 and 8051 a list of major instructions were shown in Table 2. It is observed from the Table 2 that, the instruction MVI instruction is used to load all the 8 bit registers of



(An ISO 3297: 2007 Certified Organization)

#### Vol. 4, Issue 4, April 2016

8085 microprocessor, but LXI instruction is used to load the pair of registers. The MOV instruction is used to transfer the data's from registers to registers and memory to registers and vice versa. The MOV instruction is used to load as well as to transfer data in between the registers and from memory to registers and vice versa in 8051 microcontroller. The instruction LDA 8050H of 8085 microprocessor loads Accumulator contents directly from address location 8050H or defining the memory and transferring data by using MOV A,M. This can be done by MOVX A,@DPTR in 8051 microcontroller. In place of SUB and SBB, there is a single instruction used SUBB A, Source; is used for subtraction in 8051 microcontroller.

The stack is worked on the principle of last in first out basis. In 8085, LXI SP,FFFFH instruction loads SP register with FFFFH. PUSH B, PUSH D instructions will push BC and DE pairs into stack and the SP location will be decremented by two in each PUSH instruction. Similarly POP D and POP B are used to retrieve the registers contents. In each POP instruction the SP value is incremented by two as shown in figure 3a.

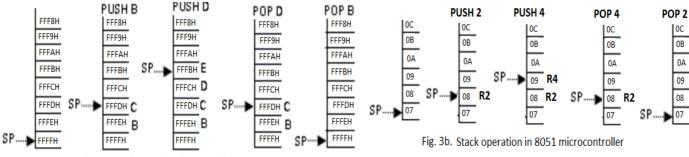


Fig. 3a. Stack operation in 8085 Microprocessor

In 8051, the default location of SP is 07. By pushing R2 & R4 registers into stack use PUSH 2 and PUSH 4 instructions and they will be retrieved by using POP R4 & POP R2 instructions. Each push will increment SP by 1 and each POP will decrement by 1 as shown in figure 3b. If bank 1 registers are used in the program, SP should be reloaded with a new location in scratch pad memory. For example to load SP with 40H use instruction MOV SP, #40H.In all 8 bit arithmetic and logical operations of both the 8085 microprocessor and 8051 microcontroller one of the operand should in Accumulator and the destination register is Accumulator only. But for 16 bit addition in 8085, one of the operand is the contents of HL pair and the destination is the contents of HL pair only. For example in DAD D instruction, (HL) = (HL) + (DE). All jump instructions in 8085 can able to jump anywhere 0000 –FFFFH. But, in 8051, the jump instructions can be classified into Long and short jumps. All long jump instructions are 3 byte instructions but all short jumps are two byte instructions only to save the program memory. Similarly the LCALL is a 3 byte instruction and ACALL is a two byte instruction only.

Table 2. Similarities and the differences in assembly language instructions of 8085 and 8051

S.No.	Assembly language instructions of 8085	Assembly language instructions of 8051			
1	Data Transfer operations: The MVI instruction is used to	MOV Destination, Source ; Destination = Source, Source			
	load the data in to a register. Ex: MVI A,05 ; $(A) = 05$ .	contents does not change. Ex 1: MOV A, $#55H$ ; (A) = 55H. The ' # ' signifies that it is a value otherwise it is a			
	MOV instruction is used to transfer either from memory to				
	register and vice versa or from register to register. IN &	memory address. Ex 2: MOV R5,#0F9H ; (R5) = F9H.			
	OUT instructions are used to transfer the data through	The 0 between # and F to indicate that F is a hex number			
	ports. LXI is used to load a register pair, STAX & STA are	and not a letter. The size of destination & Source should be			
	the instructions used to store the Accumulator contents in a	equal. Ex 3: MOV DPTR, #2040H ; (DPTR) = 2040H,			
	memory. No flags are affected with these operations.	which can also be entered as MOV DPH,#20H & MOV			
		DPL,#40H.			
2	Arithmetic operations: ADD, ADI, ADC, ACI, DAD,	(i) ADD A,Source ; $(A) = (A) + Source$ , (ii) ADDC			
	SUB, SUI, SBB are for addition and subtraction, in which	A,Source ; $(A) = (A) + (Source) + CY$ , (iii) DA A for			
	one of the data and destination is Accumulator. The INR,	decimal adjustment. (iv) SUBB A, Source ; $(A) = (A) -$			
	INX, DCR, DCX are for incrementing and decrement	(Source) – CY (v) MUL AB ; (A) = (A) x (B), Upper byte			



(An ISO 3297: 2007 Certified Organization)

### Vol. 4, Issue 4, April 2016

		(1, 1) (1, 1) (1) (1) (1) (1) (1) (1) (1) (1) (1)			
	operations. Flags are affected. DA A is for decimal	of product in (B). (vi) DIV AB; (A) = (A) / (B), the			
	adjustment of A. Multiplication can be in repeated addition	Quotient is in A and Remainder is in B register. INC, DEC			
	and division can be done in repeated subtraction process.	are for increment and decrement operations.			
3	Logic Operations: ANA, ANI, ORA, ORI, XRA, XRI	ANL Dest,Source ; (Dest) = (Dest) AND (Source),			
	and CMA instructions are used to perform bit wise logical	ORL Dest,Source ; (Dest) = (Dest) OR (Source),			
	operations. All these instructions are in relation to the	XOR Dest,Source ; (Dest) = (Dest) XOR (Source),			
	contents of Accumulator and no flags are affected	$CPLA; (A) = (\overline{A})$			
4	Branch Operations: Various types of Jump (Un	Two types of jumps, LJMP (3 byte) & SJMP (2 byte). All			
	conditional and conditional), Call and Restart instructions	conditional jumps are short jumps only. Two types of call			
	are used to change the execution sequence of the program.	LCALL (3 byte) to call subroutine anywhere in the			
	All types of Jump and Call instructions are 3 byte	memory and ACALL (2 byte) to call a routine within the			
	instructions.	page.			
5	Machine Control Operations:	ORG for origin, END for last line of a program, EQU for			
	HLT, NOP and Interrupt instructions are used to indicate	counter constant. The Define byte (DB) is used to define			
	the end of the program, no operation and interrupting the	data. It defines ASCII strings larger than two characters.			
	processor through external interrupts.	Ex: DB 0011 0101B binary, DB 39H Hex, DB "2591"			
		ASCII.			
6	Logic Operation Rotate: RLC &RAL are used to rotate	RR A; Rotate right the Accumulator contents.			
	Accumulator contents left without and with carry; RAC &	RL A; Rotate left the Accumulator contents.			
	RAR are used to rotate Accumulator contents right without	RRC A; Rotate right Accumulator contents through CY.			
	and with carry. All are one byte instructions only.	RLC A; Rotate left Accumulator contents through CY.			
	XCHG exchanges HL pair contents with DE pair contents.	SWAP A ; Swapping Accumulator bytes.			
7	Logic Operation Compare: CMP & CPI ;Compare	CJNE dest, Source, Relative address ; Compare destination			
	Accumulator contents with register / with immediate data	contents with Source contents and jump if not equal.			
	by subtracting the data from A.	Ex: CJNE A,#55,Next ; If (A) $\neq$ 55H, then jump to Next.			
	If $A < R / M$ ; $Cy = 1 \& Z = 0$ ; If $A = R / M$ ; $Cy = 0 \&$	Even though it is a conditional jump, It is a 3 byte			
	Z = 1, If  A > R / M; Cy = 0 & Z = 0	instruction. No need of involving A in compare instruction			
8	Word Size: All instructions are classified in 1, 2 and 3	All instructions classified in 1, 2 and 3 byte instructions			
	byte instructions based on whether it has only opcode or	based on whether it has only opcode or opcode followed			
	opcode followed by data or opcode followed by address.	by data or opcode followed relative address or full address.			
9	<b>CY flag bit related instructions:</b> CMC ;Compliment CY,	SETB C; CLR C; CPL C; MOV b,C; MOV c,b;			
	STC ; Set the Carry status, JC target ; jump to the target if	JNC target; JC target; target is the relative address.			
	$CY = 1$ , JNC target ; jump to the target if $CY \neq 1$	ANL c,bit ; AND CY with bit and save it on CY			
	CC target ; Call subroutine labelled by target if $CY = 1$	ANL c,/bit ; AND CY with inverted bit and save it on CY			
	CNC target ; Call subroutine labelled by target if $CY \neq 1$	ORL c,bit; OR CY with bit and save it on CY			
	$C_1 \subset C_1 \subset C_1 \subset C_1 \to C_1 $	ORL c,/bit; OR CY with inverted bit and save it on CY			
10	Zero flag bit related instructions: JZ target; jump to the	Instead of zero flag bit Accumulator contents are used.			
10	target if $Z = 1$ , target is 16 bit address. JNZ target ; jump to the	JZ ; Jump if A = 0, JNZ ; Jump if A $\neq$ 0			
	the target if $Z \neq 1$ , CZ target; Call subroutine if $Z = 1$	DJNZ; decrement and jump if $A \neq 0$			
	CNZ target ; Call subroutine labelled by target if $Z \neq 1$	CJNE A, byte ; jump if $A = byte.$			
	$C_{NZ}$ target, Call subroutine labelled by target II $Z \neq 1$	CITE A, Uyic, Jump II A – Uyic.			

**THE SIMILARITIES AND DIFFERENCES IN ADDRESSING MODES OF 8085 AND 8051:** The programmer must know the source and the destination of the data before and after execution of the instruction. The various ways of accessing data are called addressing modes. The similarities and differences in addressing modes of 8085 microprocessor and 8051 microcontroller were given in table 3. The SFRs have addresses between 80H –FFH, not all the addresses are used by SFRs. The locations 80H –FFH are reserved and must not be used by the programmer. The SFRs can be accessed by their names or by their addresses. In the absence of @ sign in register indirect addressing mode, MOV will be interpreted as an instruction moving the contents of register R0 to A, instead of the contents of memory location pointed by R0. Looping is not possible in direct addressing mode, but it is possible in register indirect addressing mode.



(An ISO 3297: 2007 Certified Organization)

### Vol. 4, Issue 4, April 2016

Table 3. The similarities and differences in addressing modes of 8085 and 8051
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S.	Addressing modes of 8085 microprocessors	Addressing modes of 8051 microcontroller			
No.					
1	Immediate Addressing Mode: The operand is specified	The operand comes immediately after the opcode. The			
	in within the instruction itself.	immediate data must be preceded by the pound sign '#'.			
	Ex: 1. MVI A,05 instruction loads A with 05 data,	Ex. 1. MOV A,#25H ; (A) = 25H			
	2. ADI 06 instruction adds accumulator contents with 06	2. MOV DPTR,#8050H ; (DPTR) = 8050H			
	and save the result in accumulator.	3. MOV P1,#55H; Transfer 55H to port 1.			
	3. LXI H,8050H loads (HL) = 8050H.	Source and destination register must be match in size.			
2	Register Addressing Mode: The operand is in one of	It involves the use of registers to hold the data to be			
	the general purpose registers, in accumulator or in	manipulated.			
	memory. The opcode specifies the address of the	Ex. 1. MOV A, R0 ; Transfer (R)) to (A)			
	registers in addition to the operation to be performed.	2. MOV R2, A; Transfer (A) to (R2)			
	Ex. 1. MOV A,B; Transfer (B) to (A). The opcode 78	3. MOV R5,R6; is an invalid instruction, if we want to			
	specifies this operation.	transfer (R6) to (R5), first transfer (R6) to (A) and then			
	2. ADD B; Add (A) with (B) and save the result in (A).	transfer (A) to (R5). Source register and destination			
	The opcode 80 specifies this operation.	register must be match in size.			
3	<b>Direct Addressing Mode:</b> The address of the operand is	The data's at 128 bytes of on chip RAM $(00 - 7FH)$ and			
	given in the instruction itself.	128 bytes external RAM (80H – FFH) can be accessed by			
	Ex: 1. STA 8050H; Store (A) in a memory location	direct addressing mode.			
	8050H, in which the three bytes 32H,50H,80H specifies	Ex. 1. MOV $R0,40H$ ; (R0) = contents of $40H = (40H)$			
	opcode, lower and higher bytes of the address.	2. MOV A,50H; (A) = contents of $50H = (50H)$			
	2. OUT 30H; Transfer (A) to port A address. The D3,	3. MOV $A,4 = MOV A,R4$			
	30H indicate the code and port A address.	4. MOV 0E0H,#55H = MOV A,#55H			
	3. IN 31H; Transfer port B contents to (A). The DB, 31H	Only direct addressing mode is allowed for pushing on to			
	indicate the code and port B address.	the stack and it must be used for POP instruction as well.			
4	Register Indirect Addressing Mode: The address of the	A register is used as a pointer to the data. Only R0 & R1			
	operand is specified by a register pair	are used for this purpose. When R0 & R1 hold the address			
	Ex. 1. LXI H, $8060H$ ; (HL) = $8060H = (M)$	of RAM locations, they must be preceded by the '@' sign.			
	MOV A,M; Move the contents of memory location,	Otherwise register contents will be transferred.			
	whose address is in HL pair to the Accumulator.	Ex. 1. MOV A,@R0; (A) = contents of $R0 = (R0)$			
	2. ADD M; (A) = (A) + (M) = (A) + (HL)	2. MOV @R1,A; contents of $R1 = (R1) = (A)$			
5	Implicit Addressing Mode: The instructions do not	Indexed addressing mode: It is widely used in accessing			
-	require the address of the operand. These instructions are	data elements of look up table entries located in the			
	meant for Accumulator only.	program ROM space of the 8051. The instruction used for			
	Ex. 1. CMA; Compliment the Accumulator contents	this purpose is MOVC A,@A+DPTR. Because the data			
	2. RAL; Rotate contents of (A) to left with carry.	elements are stored in the program (Code) space, it uses			
	3. RAR; Rotate contents of (A) to left with carry.	MOVC instead of MOV. The 'C' means code.			
L	5. It it, Rotate contents of (A) to fold with cally.				

**SIMILARITIES AND DIFFERENCES IN ASSEMBLY LANGUAGE PROGRAMS OF 8085 AND 8051:** The assembly language program of 8085 microprocessor is appeared in five field format <sup>(7)</sup>. The address field, opcode field, Label field, mnemonic field and the comments/ description field. But the assembly language program of 8051 can be appeared as four field format only. The Assembly language program for 16 bit addition and multiplication of two hexadecimal numbers for both 8085 and 8051 were given Table 4.



(An ISO 3297: 2007 Certified Organization)

### Vol. 4, Issue 4, April 2016

Table 4. The similarities and differences in assembly language programs of 8085 and 8051

Assembly language programs of 8085 microprocessors				Assembly language programs of 8051 microcontroller				
1. To write and execute an ALP of 8085 microprocessor to add two				1. To write and execute an ALP of 8051 to add two 16 bit				
16 bit data, stored from the memory locations 8050H using Double				data, stored from the RAM memory locations 50H and store				
Addition (DAD) instruction. The program is given below:							program is given below:	
radition (Drid) instruction. The program is given below.			ORG 8000 ; origin address of the program					
Address	Opcode	Lbl	Mnemonic	Comments	Addr	Lbl	Mnemonic	Comments
8000H	2A,50,80		LHLD 8050H	(HL) = (8050H)	8000H		MOV R3,#0	;(R3) = 00
8003H	EB		XCHG	$(DE) \leftrightarrow (HL)$	8002H		MOV R0,#50H	;(R0)=50H=LB of D1
8004H	2A,52,80		LHLD 8052H	(HL) = (8052H)	8004H		MOV A,@R0	; (A) = LB of D1
8007H	0E,00		MVI C,00	(C) = 00,	8005H		INC R0	;(R0)=51H= LB of D2
8009H	19		DAD D	(HL)=(HL)+(DE)	8006H		ADD A,@R0	; (A) = (A) + (51H)
800AH	D2,0E,80		JNC NEXT	If CY=0, to nxt	8007H		MOV R2,A	; $(R2) = Sum of LBs$
800DH	0C		INR C	If CY=1, store in C	8008H		INC R0	;(R0)=52H=HB of D1
800EH	22,0E,80		SHLD 8054H	(HL) stored in	8009H		MOV A,@R0	; (A)= HB of Data1
				8054H & 8055H	800AH		INC R0	;(R0)=53H=HB of D2
8011H	79	nxt	MOV A,C	$(\mathbf{A}) = (\mathbf{C}) = \mathbf{C}\mathbf{Y}$	800BH		ADDC A,@R0	(A)=(A)+(53H)+CY
8012H	32,56,80		STA 8056H	(A) = (8056H) = CY	800CH		JNC NXT	;If CY=0, jump to nxt
8015H	76		HLT	Halt the program	800EH		INC R3	; If CY=1, store in R3
				1 0	800FH	nxt:	INC R0	;(R0) = 54H
				l in 8050H & 8051H	8010H		MOV @R0,R2	; $(54H) = $ Sum of LBs
			d in 8052H & 8053		8011H		INC R0	(R0) = 55H
			s for Lower byte a		8012H		MOV @R0,A	; $(54H) = $ Sum of HBs
				re stored in 50H and	8013H		INC R0	(R0) = 56H
	51H and higher bytes are stored in 52H and 53H. If the instruction				8014H		MOV @R0,R3	; $(56H) = CY$ condition
			, first transfer RX	to A and then	8015H		LCALL 0003	; End of the program
	transfer to the RAM memory and vice versa.							
	2. To write and execute an ALP of 8085 microprocessor to multiply two 8 bit data's stored in 8050H the program is given below:				2. To write and execute an ALP of 8051 to multiply two 8 bit data's stored in 50H the program is given below:			
Address	Op	Label	Mnemonic	Comments	Address	Lab	Mnemonic	Comments
	code					el		
8000H			LXI H,8050H	(HL)=(8050H)	8000H		MOV R0,#50H	;(R0)=50H= Data1
8003H			MVI B,00	(B) = 00, for CY	8002H		MOV A,@R0	; (A)= Data1
8005H			MOV A,B	(A) = 00	8003H		INC R0	;(R0)=51H= Data2
8006H			MOV C,M	$(\mathbf{C}) = \mathbf{Data} \ 1$	8004H		MOV 0F0,@R0	; (B)= Data2
8007H			INX H	(HL)=(8051H)	8006H		MUL AB	; $(A) = (A) \times (B)$
8008H			MOV D,M	(D) = Data 2	8007H		INC R0	;(R0)=52H
8009H		BCK:	ADD D	(A) = (A) + (D)	8008H		MOV @R0,A	(52H) = LB  of product
800AH			JNC NXT	If CY=0, to NXT	8009H		INC R0	;(R0)=53H
800DH			INR B	If CY=1, store in B	800AH		MOV @R0,0F0	(53H) = HB of product
800EH		NXT:	DCR C	Decrement C	800CH		LCALL 0003	; End of the program
800FH			JNZ BCK	If C≠0, to BCK				
8012H			INX H	(HL) = 8051H	_	a -		
8013H			MOV M,A	(8051H) = Product				
8014H			INX H	(HL) = 8052H				ith 8051 microcontroller
8015H			MOV M,B	(8052H) = CY	than 8085 microprocessor. It saves the memory space of the			
8016H			HLT	Halt the program	central processing unit			



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#### Vol. 4, Issue 4, April 2016

### **IV. CONCLUSIONS**

1. The 8085 is the most useful 8 bit general purpose microprocessor and the 8051 is the most familiar and useful 8 bit microcontroller used for controlling applications.

2. A powerful feature of 8051 I/O ports is their capability to access individual bits of the port without altering the rest of the bits in that port. Many special functional registers have the capability to access individual bits without altering other bits in the register.

3. Compare with 8085 microprocessor, 8051 microcontroller has some additional instructions used for setting and resetting the individual bits of single bit accessible registers like A, PSW, TCON, TMOD, SCON etc.;

4. The SP register is an 8 bit register in 8051 microcontroller and direct addressing mode is used to push / pop the contents in stack memory.

5. All conditional jump and all conditional call instructions of 8051 are 2 byte instructions, but all types of jump and all call instructions of 8085 are 3 byte instructions only.

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