



ISSN(Online) : 2320-9801
ISSN (Print) : 2320-9798

International Journal of Innovative Research in Computer and Communication Engineering

An ISO 3297: 2007 Certified Organization

Vol.4, Special Issue 3, April 2016

3rd National Conference on Emerging Trends In Electronics and Communication Engineering (NCETECE'16)

Organized by

Dept. of ECE, New Prince Shri Bhavani College of Engineering & Technology, Chennai-600073, India

Analysis of LT Distribution Losses by SCADA Load Flow Techniques

E.Lakshmanan, Dr.M.Kumaresan

Research Scholar, Department of EEE, Dr. MGR Educational and Research Institute University, Chennai, Tamil Nadu, India

Professor, Department of EEE, Dr. MGR Educational and Research Institute University, Chennai, Tamil Nadu, India.

ABSTRACT: In this paper an attempt to save the energy losses in low tension distribution networks by SCADA (Supervisory Control and Data Acquisition) load flow techniques are utilized. The electricity distribution control system faces a major problem of distribution losses due to usage of analogy meters, inadequate size conductors, cables, lengthily distribution lines, failure of sub-station equipments, improper earthing, transformer failure etc., Therefore, the main objective of this paper is to calculate the energy losses in power transformers, distribution transformers and 11KV Level Cross feeder circuits in Chennai Electricity Distribution Control Centre-SCADA networks. The results are compared with theoretical and practical values of energy losses in power circuits using Mean Absolute Percentage Error (MAPE) method. The proposed method is increases the good quality of power factor and reduce voltage drop at tail end of distribution feeder circuits. In this study of sample survey was conducted based on constitution Indian Electricity Act-2003 and follow up Tamil Nadu Electricity Board Rules and Regulations [1].

KEYWORDS: Distribution Transformers losses, 11 KV Feeder losses, load factor, load loss factor, load curve and load duration curve, MAPE, Power Transformers losses.

I. INTRODUCTION

The Chennai electricity distribution control centre consists of a group of ring main feeder with the voltage levels of 400KV, 230KV, 110KV, 33KV, 11KV and LT lines which can be mutually inter-linked with several back feeding arrangements. The energy consumed in distribution system at various techniques is involved. In this SCADA load flow technique is describe the future expansion of existing electricity distribution networks and monitoring the load flow or power flow in electrical generators, transmission lines and distribution feeder networks. The proposed method is find out energy losses in a pair of power transformers, distribution transformers and 11 KV Level Cross feeder circuits are discussed. In this paper, a method that includes the availability of load and corresponding to the SCADA Multi Function Transducer (MFT) values are considered from 33 KV Saidapet sub-station, Chennai Electricity Distribution Circle (CEDC) for the period of one month. The SCADA MFT Telemeters values may used to calculate actual energy losses in the power transformers, distribution transformers and 11 KV Level Cross feeder circuits are discussed as follows.

II. SCADA LOAD FLOW TECHNIQUES

The load flow studies are probably the most common of all power system analysis measurements; they are used in planning studies to determine if and when specific elements will become overloaded or under loaded. The major investment decisions begin with reinforcement strategies based on the load flow or power flow analysis. In operating studies, load flow analysis is used to ensure that each generator run at the optimum operating point; that the demand will be met without overloading facilities; and maintenance plans can proceed without undermining the security of the system level.



Organized by

Dept. of ECE, New Prince Shri Bhavani College of Engineering & Technology, Chennai-600073, India

II (a) Function SCADA control centre

The main function of SCADA control centre in load flow techniques are:

- i. Supervisory control of operation, Data acquisition from RTU and storage of data in online,
- ii. Providing user interface from operators, Voltage magnitude and phase angle at each bus
- iii. Real and reactive power flow in each bus, Reactive power loading on each generator
- iv. Power system planning and control, Historical and real time events
- v. Sequential mode of operation, Protective and information tagging of electrical equipments which are under out of service

II (b) Data obtained to identify the following:

- i. Normal/Emergency mode of operation, Break/backup operation mode, Contingency analysis & Outage security assessment
- ii. Optimum load dispatch, Stability analysis, Load flow analysis, Power flow analysis, & Optimum power flow analysis
- iii. State estimation, Economics dispatching, Security analysis, Rescheduling & Load management

II (c) Benefit of SCADA load flow technique

- i. To increase reliability and availability of power system
- ii. To eliminates the manual mode of operation
- iii. Effective load management of Distribution Network
- iv. Close monitoring of supply status for essential services & VIP functions
- v. Prompt restoration of supply in case of outages.
- vi. Quick change over of supply while normalizing the feeding condition.
- vii. Malfunctioning of breakers and protection system can be identified and action can be taken for rectifying the same early.
- viii. Breakers which are tripping frequently can be identified and maintenance work can be taken up in time.
- ix. Existing manned substations could be completely controlled from the Distribution Control Centre (DCC), after executing the necessary improvements i.e., they can be operated as unmanned substations.
- x. As real time load details will be available for fault analysis can be made effectively.
- xi. Reduce maintenance cost

III. TRANSFORMER AND FEEDER CIRCUITS

The electrical energy is received from generating station to substation and end use of customer at various voltage level of EHT to LT networks. In this paper, energy losses in real time SCADA electricity distribution networks are discussed as follows.

- i. Energy losses power transformer
- ii. Energy losses in feeder circuits
- iii. Energy losses in distribution transformers
- iv. Comparison statement of theoretical Vs practical values
- v. Mean Absolute Percentage Error (MAPE) values

In this 33 KV Saidapet sub-station primary supply received from 110 KV Velachery sub-station and 110 KV Nandanam Sub-station via 33 KV MHU Compound sub-station, The primary distribution supply feeds a pair of 33/11KV-16 MVA power transformer I & II, In addition to that eight numbers 11 KV feeder circuits are parallel connected to 16 MVA power transformers-LV1 and LV2. The analysis of 16 MVA power transformers I & II, 11 losses KV Level Cross feeder circuits with 14 No's distribution transformers are considered as calculation of energy in whole power circuits. The systematic singleline diagram of 33 KV Saidapet sub-station as shown in Fig.1.

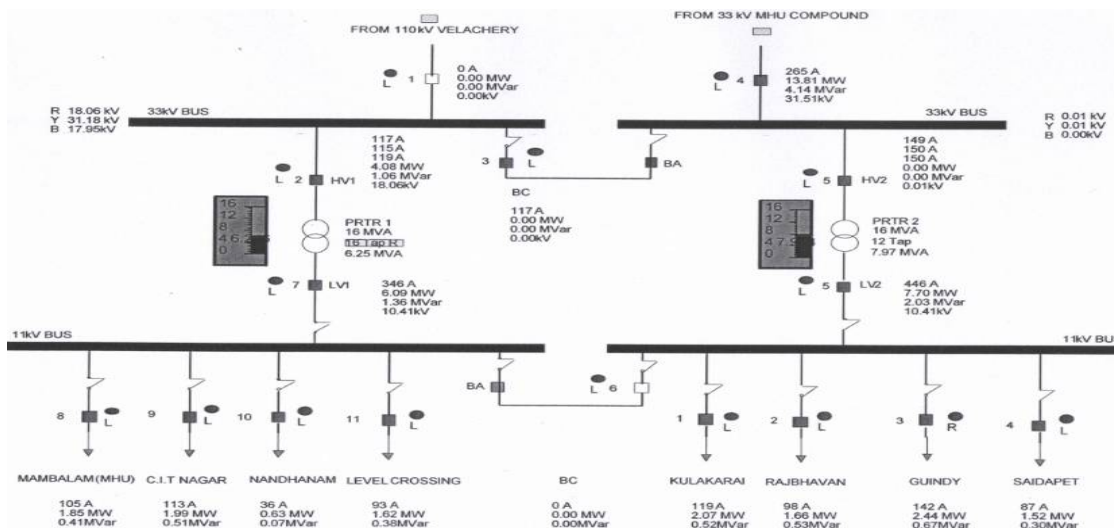


Fig.1 KV Saidapet sub-station power transformers and feeder circuits

VI. Load and Load Duration Curve for 11 KV Level Cross feeder circuits and Power Transformers I&II

The actual load and load duration curve for typical 16 MVA power transformers I & II, 11 KV Level Cross feeder circuits in Saidapet sub-station as shown in Figure.2 and Figure.3 and also theoretical and practical values of energy loss calculation as shown in Table.3 and Table.4 and Table.5 load data's obtained from Table.2, The real time values are implemented in ETAP software program to get energy losses in MW, MVar and Voltage drop at each and every point of nodes as shown in Figure.4.and Table.6

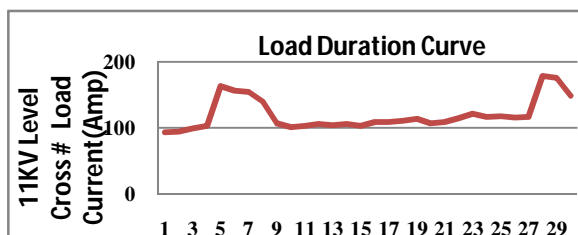


Fig.2 Load and Load duration curve for 11 KV Level Cross feeders

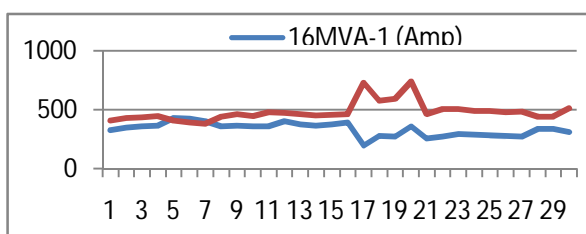


Fig.3 MVA Power Transformers I & II Load Duration Curve

Energy consumption losses in Feeder Circuits in MU 11KV Feeder = $3 * I^2 * R * L * LLF * 24 * 30 * 10^{-6}$ (8)

Where

$LLF = LF^2 + 0.273 * (LF - K)^2$



Organized by

Dept. of ECE, New Prince Shri Bhavani College of Engineering & Technology, Chennai-600073, India

Energy consumption losses in Power Transformer in MU

$$\text{Power Transformer} = \frac{\text{No load} + [(\% \text{ Loading})^2 * \text{Conductor .cons tan } t * \text{LLF}] * 8760}{10^6} \quad (9)$$

The Mean Absolute Percentage Error (MAPE) is difference between the theoretical and practical values of energy losses divided by theoretical energy losses in power circuits. This method is used to find out accuracy of distribution losses in real time theoretical and practical values of Chennai Electricity Distribution Control Centre, SCADA networks.

$$\text{MAPE} = \frac{1}{N} \sum_r \frac{|T_v - P_v|}{T_v}$$

Where

N-Total period of calculation

Tv-Theoretical energy losses in power circuits

Pv-Practical energy losses in power circuits

Sl.No	Feeder & Power Transformer Load in Amp			
	Day(Hrs)	Level Cross	16MVA-1	16MVA-2
1	1	93	327.6	405.00
3	2	94.2	353.7	429.30
4	3	98.7	361.8	433.80
5	4	102.3	367.2	444.60
6	5	163.2	432.9	405.90
7	6	156.6	425.7	391.50
8	7	154.2	405	379.80
9	8	139.8	362.7	437.40
10	9	106.2	366.3	461.70
11	10	100.8	363.6	448.20
12	11	102.6	360.9	476.10
13	12	105.6	406.8	471.60
14	13	104.1	379.8	462.60
15	14	105.3	368.1	450.00
16	15	102.9	376.2	459.00
17	16	108.3	394.2	464.40
18	17	108.9	198.9	728.10
19	18	110.1	279	576.90
20	19	113.4	275.4	591.30
21	20	106.2	360.9	741.60
22	21	108.3	260.1	461.70
23	22	114.9	273.6	505.80
24	23	120.9	294.3	507.60
25	24	116.4	290.7	486.90
26	25	117	283.5	486.90
27	26	115.2	282.6	477.00
28	27	116.4	276.3	484.20
29	28	178.8	338.4	438.20
30	29	176	340.2	438.30
31	30	148.2	313.2	513.90

Table.1 Average Load details in 11 KV Feeder and Power Transformers



Organized by

Dept. of ECE, New Prince Shri Bhavani College of Engineering & Technology, Chennai-600073, India

Energy losses in Feeder Circuit		
LF=(Average Load)/(Maximum Load)		
LLF=K*LF=(1-K)*LF ² (OR) 0.2*LF+0.8*LF ²		
Technical loss=3*I ² *R*Length*LLF*24*30*10 ⁻⁹		
Feeder name	11KV Level cross	
Length of Cable	9.72	KM
Peak Load	178.8	Amp
Average Load	150	
Load Factor	0.84	
Load loss Factor(0.20)	0.73	
Technical losses in Feeder	0.696	MU

Table.2 Theoretical values of energy losses in 11 KV Level Cross feeder circuits in MW

Energy losses in Power Transformers		
LF=(Average Load)/(Maximum Load)		
LLF=K*LF=(1-K)*LF ² (OR) 0.2*LF+0.8*LF ²		
Technical loss for Power transformer={No Load loss+[(% of Loading) ² *Rated Cu		
Power transformer Capacity	16	MVA
Full Load Capacity	840	Amp
Power Transformer 11KV Peak Load	741.6	Amp
% of Peak Load	0.88	
Load Factor	0.81	
Load loss Factor(0.20)	0.69	
Standard losses of Power Transformer		
No Load losses	16.8	KW
Full(Cu) Load losses	68	KW
Technical losses in Power Transformer in MU	0.47	MU

Table.3 Theoretical values of energy losses in Power transformer I & II

Energy losses in Distribution Transformers				
LF=(Average Load)/(Maximum Load)				
LLF=K*LF=(1-K)*LF ² (OR) 0.2*LF+0.8*LF ²				
After Load Bifurcation: Technical losses in Transformer				
Technical loss for Power transformer={No Load loss+[(% of Loading) ² *Rated Cu loss]}*LLF*24*365*10 ⁻⁵				
Description of Calculation				
Transformer Capacity	100	250.00	500	KVA
Full Load Capacity	84	210.00	420	Amp
Transformer 11KV Peak	79.8	157.50	273	Amp
% of Peak Load	0.95	0.75	0.65	%
Load Factor	0.475	0.38	0.325	
Load loss Factor(0.20)	0.2755	0.19	0.1495	
Standard losses of Transforn				
No Load losses	0.3	0.50	0.9	KW
Full(Cu) Load losses	1.2	2.00	3.5	KW
Technical losses in Power Transformer in MU	0.00	0.00	0.003	MU

Table.4 Theoretical values of energy losses in Distribution transformers I & II

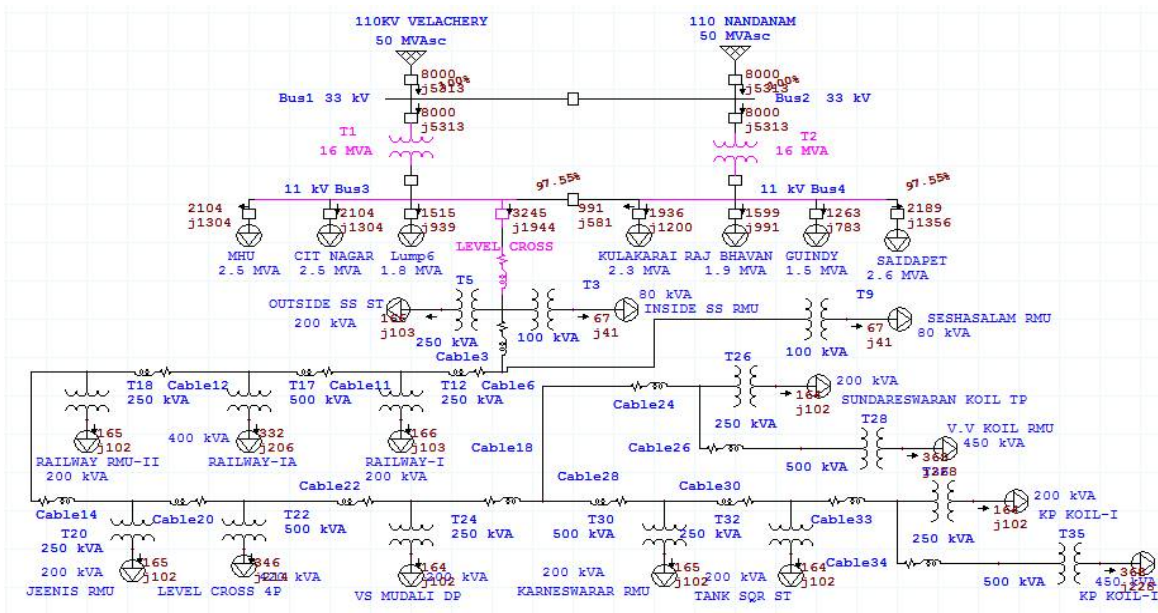


Fig.4. ETAP diagram of 33 KV Saidapet sub-station power transformers I&II and 11 KV Level cross feeder circuits

Study Reports		Study ID	Untitled
Ref.	Select	Study Case ID	LF
	<input checked="" type="checkbox"/>	Data Revision	Base
		Configuration	Normal
		Loading Cat	Design
		Generation Cat	Design
		Diversity Factor	Normal Loading
Project Report		Buses	34
<input checked="" type="radio"/> Active Project <input type="radio"/> All Project in Active Directory		Branches	34
Report Type		Generators	0
<input checked="" type="radio"/> General Info <input type="radio"/> Bus Results <input type="radio"/> Branch Results <input type="radio"/> Loads <input type="radio"/> Sources		Power Grids	2
		Loads	22
		Load-MW	15.999
		Load-Mvar	10.627
		Generation-MW	15.999
		Generation-Mvar	10.627
		Loss-MW	0.257
		Loss-Mvar	0.871
		Mismatch-MW	0
		Mismatch-Mvar	0

Table.5 Actual energy consumption losses in whole power circuits by ETAP software program

V. RESULT

The case study was successfully conducted and calculated average energy losses in power circuits is 1.131 Million Units per annum was dissipated in 16 MVA power transformers I & II, 11 KV Level Cross feeder circuits and 14Nos distribution transformers by Electrical Transient and Analysis soft program and also comparative statement of theoretical and practical values are as shown in Table.6



Organized by

Dept. of ECE, New Prince Shri Bhavani College of Engineering & Technology, Chennai-600073, India

Sl.No	Energy losses in power circuits	Theoretical Values in MU	ETAP Programming in MU
1	Power Transformers	0.47	0.537
2	11KV feeders	0.696	0.591
3	14Nos Distribution Transformers	0.05	0.003
4	Total Energy losses per Annum	1.216	1.131

Table.6 Nett energy consumption in theoretical and practical values

The theoretical and practical values are almost equal and achieving of Mean Absolute Percentage Error is **0.70** only.

VI. CONCLUSION

This paper discussed to analysis of energy losses in a SCADA Electricity Distribution Network. These measures include load switching, feeder reconfiguration, load reduction and voltage control. The use of this application improves the reliability and efficiency of the transmission and distribution network.

By proper selection of transformers, feeders, proper re-organization of distribution network, reducing the length of LT lines, bifurcating of HT lines, placing the compensating equipments in appropriate places, that may reduce the distribution losses and also training of the operating personal would result in improve the efficiency of system.

VII. FUTURE WORK

The existing Transmission and distribution feeder circuits are appropriate rating and distribution transformers are installed at nearer to load centre, which becomes good quality of power factor raised in consumer's terminal or tail end of distribution transformer. For accurate calculation of energy losses, energy meters should be connected to all distribution transformers. Installing shunt capacitors of optimum rating at vantage point of substation, line losses in LT networks may considerably reduced.

REFERENCES

- [1] Power Engineers Hand Book, TNEB Engineers Association, No: 144, Anna salai, Chennai-600002.
- [2] Sarang Pande and Prof. Dr.J.G. Ghodekar, "Computation of Technical Power Loss of Feeders and Transformers in Distribution System using Load Factor and Load Loss Factor", international journal of multidisciplinary sciences and engineering, vol. 3, no. 6, June 2012
- [3] Ali Nourai, Senior Member, IEEE, V.I.Kogan, Senior Member, IEEE, and M.Schafer, Senior Member, IEEE, "load levelling reduces T&D line losses",0885-8977/\$25.00©2008 IEEE.
- [4] Sandhya Gour, Dr.Bharti Dwivedi, "Feeder Renovation in Electric Power System for Reduction of T&D losses", ISSN: 2347-8446, 2347-9817
- [5] V.A.Kulkarni and P. K Katti, "Estimation of Distribution Transformer Losses in Feeder Circuit" International Journal of computer and Electrical Engineering,vol.3,No.5,October 2011.
- [6] Surabhi Jain and Ranjana Singh, "Enhancement of the Distribution system by Implementing LT-Less Distribution Technique" International Journal of Scientific and Research Publications, Volume 3,Issue 10,October-2013,ISSN 2250-3153.
- [7] Anita Gupta, Harmeet Singh Gill and Isha Bansal, "Effectiveness of High Voltage Distribution System" IOSR Journal of Electrical and Electronics Engineering,ISSN:2278-1676 Volume 1,Issue 5 (July-Aug.2012).PP34-38
- [8] Narong Mungkung, Nittaya Gomurut, Tanes Tanitteerapan, Somchai Arunrungrusmi, Weerachai Chaokumnerd and Toshifumi Yuji, Analysis of Technical Loss in Distribution Line System, Proceedings of the 8th WSEAS International Conference on Telecommunications and Informatics, ISSN:1790-5117,ISBN:978-960-474-084-0

BIOGRAPHY



First Author: E.Lakshmanan received his B.E degree in Electrical and Electronics Engineering from Adhiparasakthi College of Engineering, Madras University, Tamil Nadu, India in 1998, M.TECH Degree in Power System Engineering from Dr.M.G.R. Educational and Research Institute University, Chennai, India in 2013, Pursuing PhD in Power system Engineering at Dr.M.G.R.Educational and Research Institute University, Chennai, India. He has a wide Practical and SCADA experience from Chennai Electricity Distribution control centre, and presently working in Tamil Nadu Generation and



ISSN(Online) : 2320-9801
ISSN (Print) : 2320-9798

International Journal of Innovative Research in Computer and Communication Engineering

An ISO 3297: 2007 Certified Organization

Vol.4, Special Issue 3, April 2016

3rd National Conference on Emerging Trends In Electronics and Communication Engineering (NCETECE'16)

Organized by

Dept. of ECE, New Prince Shri Bhavani College of Engineering & Technology, Chennai-600073, India

Distribution Corporation Limited (TANGEDCO), Chennai, India. He has guided many projects at U.G. & P.G level.



Second Author: Dr.M.Kumaresan completed Bachelor of Engineering in Electrical and Electronics Engineering from University of Madras, M.Tech.in Power System from Annamalai University and Ph.D. in 2013 from Dr.MGR Educational and Research Institute, University, Chennai. He is a Professor, department of EEE, Dr. MGR Educational and Research Institute University, Chennai. He has a teaching experience for twelve years. His research areas include Flow control for non-linear power plant and HVDC Transmission and Power system operation and control etc. He has guided projects at U.G., P.G and PhD level

PIC16F877 is 40 pin IC. There are six ports in this microcontroller namely PORT A, PORT B, PORT C, PORT D and PORT E. Each pins in the ports can be used as either input or output pins.